

# Improved Performance of Single-Phase Seven-Level Grid-Connected Inverter for PV System

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**Abstract:**

This article presents the performance of single phase seven level grid connected inverter for PV system. The performance of the designed model is checked with help of MATLAB/Simulink. The results are presented to show the proficient of the designed model.

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## 1. Introduction and Working of Proposed Inverter

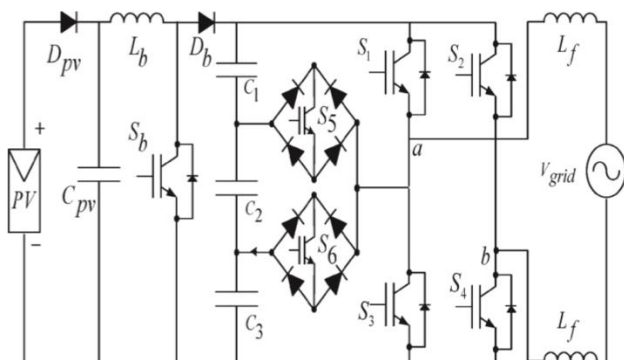


Fig. 1. Seven-level grid-connected inverter for photovoltaic systems

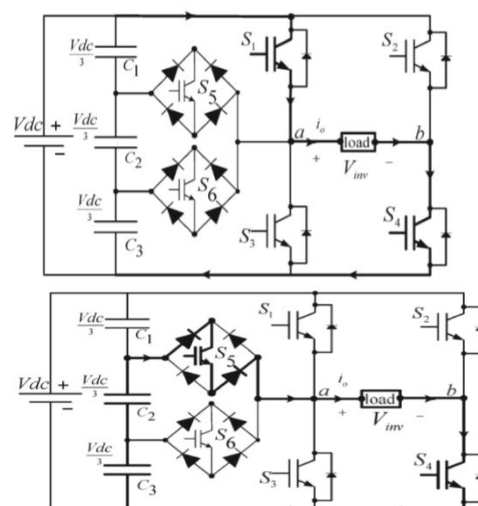


Table 3.1 shows the switching combinations that generated the seven output-voltage levels ( $0, -V_{dc}, -2V_{dc}/3, V_{dc}/3, V_{dc}, 2V_{dc}/3, V_{dc}/3$ )

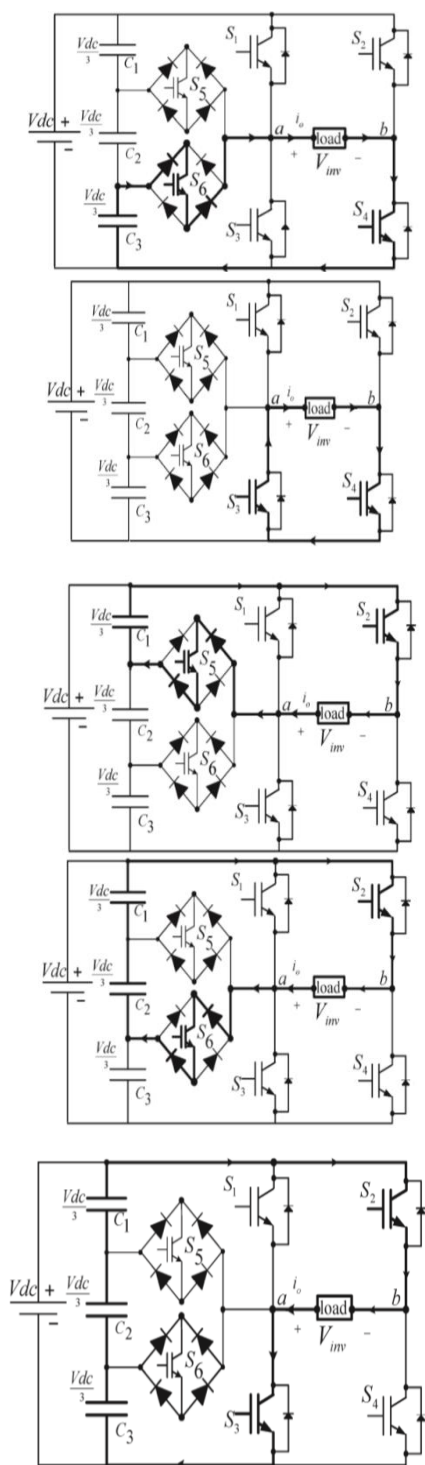


Fig. 2. Switching Pattern.

TABLE 3.1

OUTPUT VOLTAGE ACCORDING TO THE SWITCHES' ON-OFF CONDITION

$v_0$	$S_1$	$S_2$	$S_3$	$S_4$	$S_5$	$S_6$
$V_{dc}$	on	off	off	on	off	off
$2V_{dc}/3$	off	off	off	on	on	off
$V_{dc}/3$	off	off	off	on	off	on
0	off	off	on	on	off	off
$0^*$	on	on	off	off	off	off
$-V_{dc}/3$	off	on	off	off	on	off
$-2V_{dc}/3$	off	on	off	off	off	on
$-V_{dc}$	off	on	on	off	off	off

### 3.3 PWM Modulation

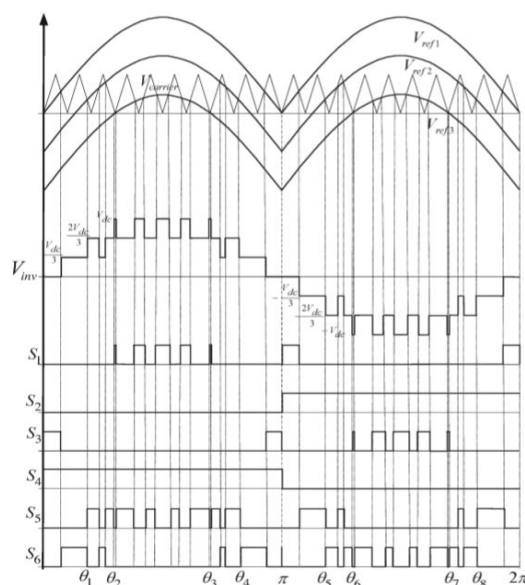


Fig. 3.3. Switching pattern for the single-phase seven-level inverter.

A novel PWM modulation technique was introduced to generate the PWM switching signals. Three reference signals ( $V_{ref1}$ ,  $V_{ref2}$ , and  $V_{ref3}$ ) were compared with a carrier signal ( $V_{carrier}$ ). The reference signals had the same frequency and amplitude and were in phase with an offset value that was equivalent to the amplitude of the carrier signal. The reference signals were each compared with the carrier signal. If  $V_{ref1}$  had exceeded the peak amplitude of  $V_{carrier}$ ,  $V_{ref2}$  was compared with  $V_{carrier}$  until it had exceeded the peak amplitude of  $V_{carrier}$ . Then, onward,  $V_{ref3}$  would take charge and would be compared with  $V_{carrier}$  until it reached zero. Once  $V_{ref3}$  had reached zero,  $V_{ref2}$  would be compared until it reached zero. Then, onward,  $V_{ref1}$  would be compared with  $V_{carrier}$ .

Fig. 3.3 shows the resulting switching pattern. Switches  $S_1$ ,  $S_3$ ,  $S_5$ , and  $S_6$  would be switching at the rate of the carrier signal frequency, whereas  $S_2$  and  $S_4$  would operate at a frequency that was equivalent to the fundamental frequency.

#### 4 Simulation Results

This part discusses about simulation results of seven level inverter (single phase). The dc-bus voltage was set at 900 V ; in this case,  $V_{grid}$  was 900V peak -peak).  $F_s=5kHz$ . Fig. 4 show the MATLAB/Simulink model of the designed system. Figs. 3.5 to 3.8 simulated responses of the designed system.

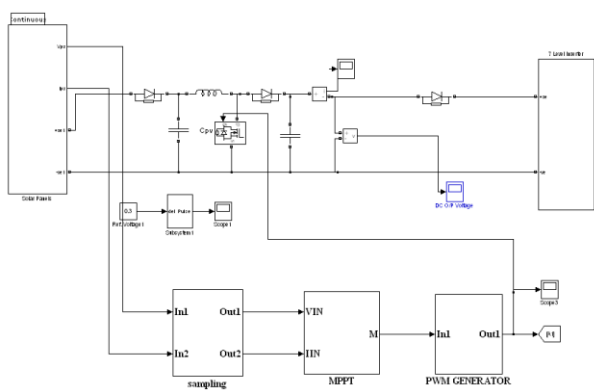


Fig. 4. MATLAB/Simulink model of the single-phase seven-level inverter.

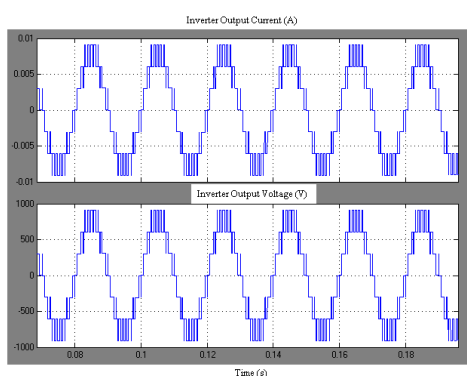


Fig. 5. Simulated response of single-phase seven-level inverter output voltage and current.

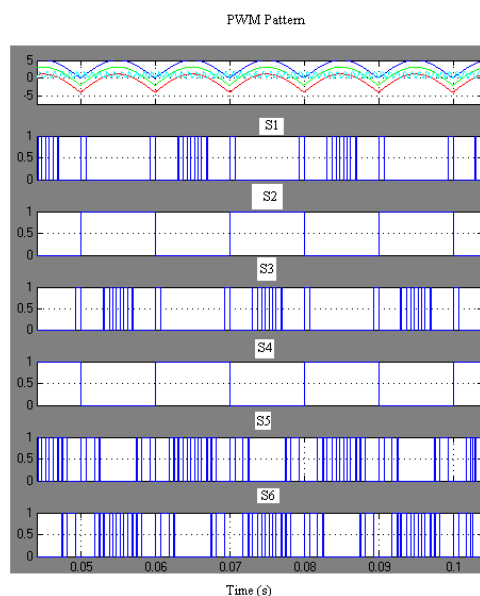


Fig. 6. Simulated response of gating pulse for single-phase seven-level inverter.

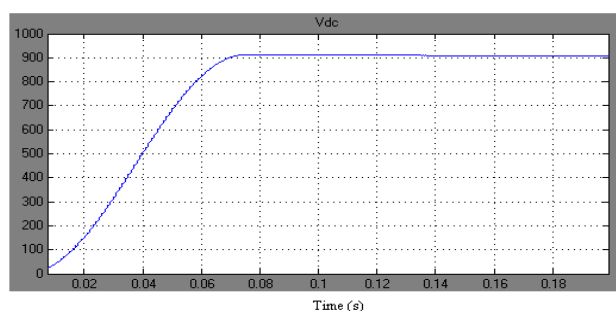


Fig. 7. Simulated response of gating pulse for single-phase seven-level inverter.

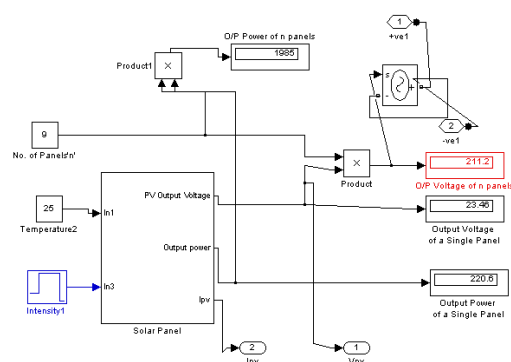


Fig. 8. Simulated response of PV details.

#### Conclusions

Multilevel inverters (MLI) offer improved output waveforms and lower THD. This work has

presented a novel PWM switching scheme of MLI. It utilizes three reference signals and a triangular carrier signal to generate PWM switching signals. The behaviour of the proposed multilevel inverter was analyzed in detail. By controlling the modulation index, the desired number of levels of the inverter's output voltage can be achieved.

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